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APPLICANTS

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\*\* CONTINUING DATA \*\*\*\*\*

This appln claims benefit of 60/406,137 08/26/2002 *BT*

\*\* FOREIGN APPLICATIONS \*\*\*\*\* *BT*

IF REQUIRED, FOREIGN FILING LICENSE GRANTED

\*\* 09/22/2003

Foreign Priority claimed 35 USC 119 (a-d) conditions met	<input type="checkbox"/> yes <input checked="" type="checkbox"/> no <input type="checkbox"/> yes <input checked="" type="checkbox"/> no <input type="checkbox"/> Met after	STATE OR COUNTRY CANADA	SHEETS DRAWING 6	TOTAL CLAIMS 15	INDEPENDENT CLAIMS 1
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Verified and Acknowledged

Examiner's Signature *Butler* Initials *BT*

ADDRESS

24309  
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TITLE

Post-layout optimization in integrated circuit design

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